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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/628,718	07/28/2000	Tadayuki Sakakibara	500.38828X00	3342

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EXAMINER

PEIKARI, BEHZAD

ART UNIT PAPER NUMBER

2186

DATE MAILED: 11/26/2004

Please find below and/or attached an Office communication concerning this application or proceeding.



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APPLICATION NO./ CONTROL NO.	FILING DATE	FIRST NAMED INVENTOR / PATENT IN REEXAMINATION	ATTORNEY DOCKET NO.
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EXAMINER

ART UNIT	PAPER
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20041122

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Commissioner for Patents

The reply filed on September 21, 2004 is not fully responsive to the prior Office Action because of the following omission(s) or matter(s): the required substitute specification has not been submitted.

As stated in the previous Office action, the specification is replete with errors in English Idiom. Although much of the subject matter can be understood by one of ordinary skill in the art by carefully re-reading many sections of the text, the awkward language of the text makes this very difficult.

For example, the second paragraph of the specification might be properly rewritten as follows:

"In recent years, the microprocessors (CPU) speeds have increased at a faster rate than the speed of memory devices, and this performance gap continues to widen. Cache memories designed to operate at speeds higher than those of main memories are used to alleviate performance differences by shortening effective memory access times."

See 37 CFR 1.111. Since the above-mentioned reply appears to be *bona fide*, applicant is given **ONE (1) MONTH or THIRTY (30) DAYS** from the mailing date of this notice, whichever is longer, within which to supply the omission or correction in order to avoid abandonment. EXTENSIONS OF THIS TIME PERIOD MAY BE GRANTED UNDER 37 CFR 1.136(a).

B. James Peikari
Primary Examiner
Art Unit: 2186